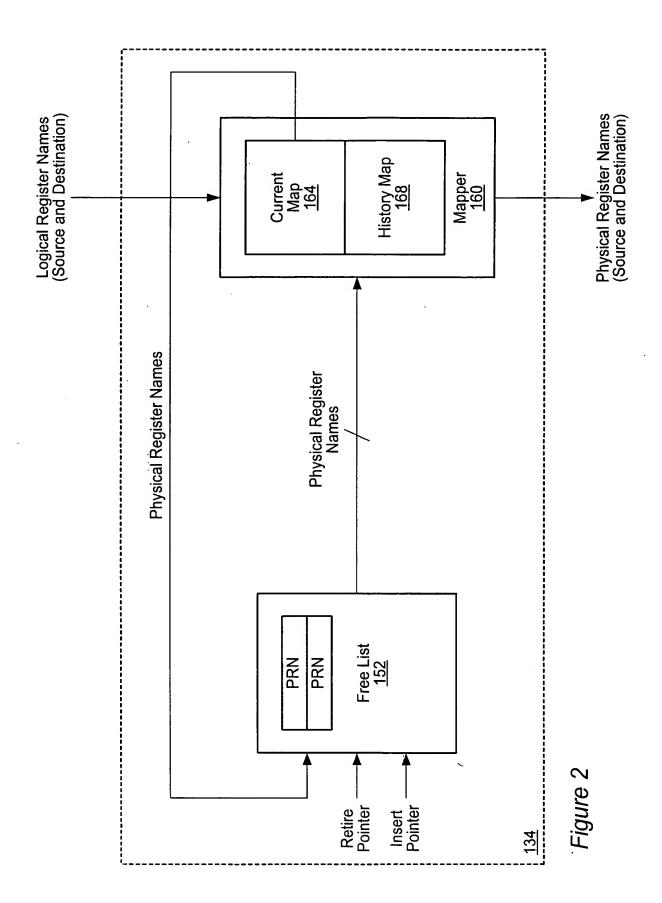
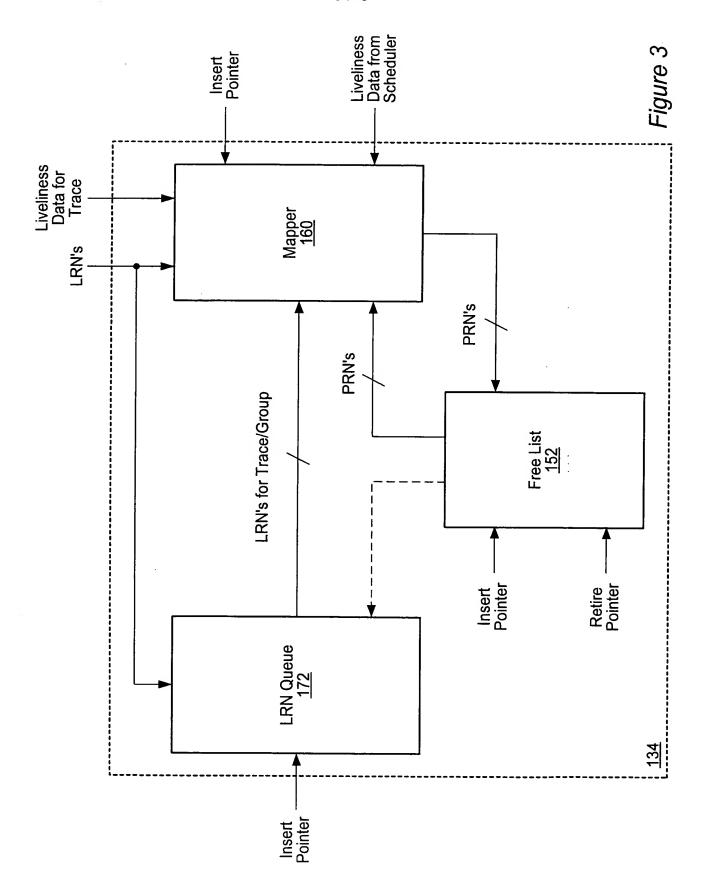
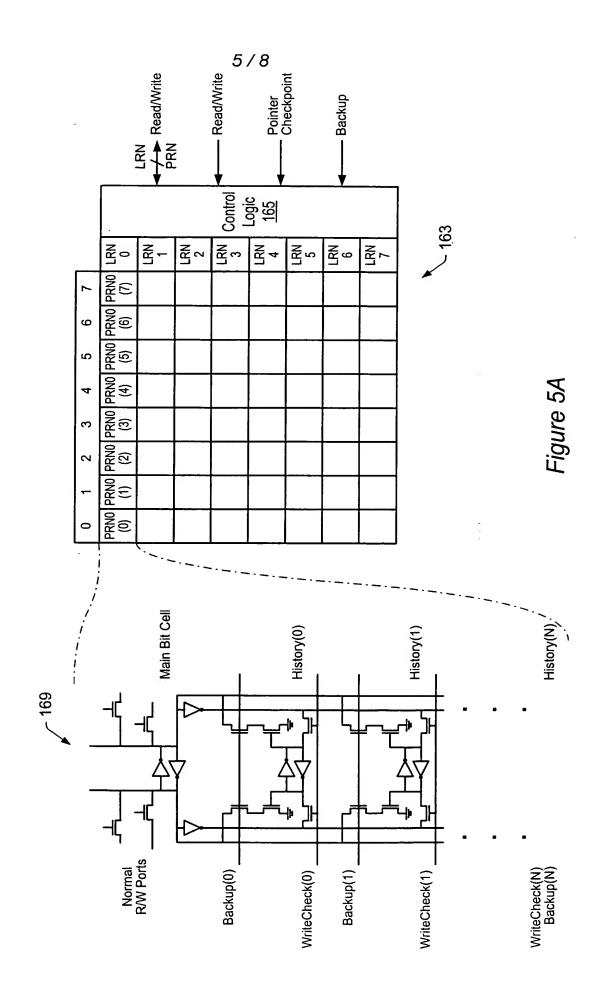


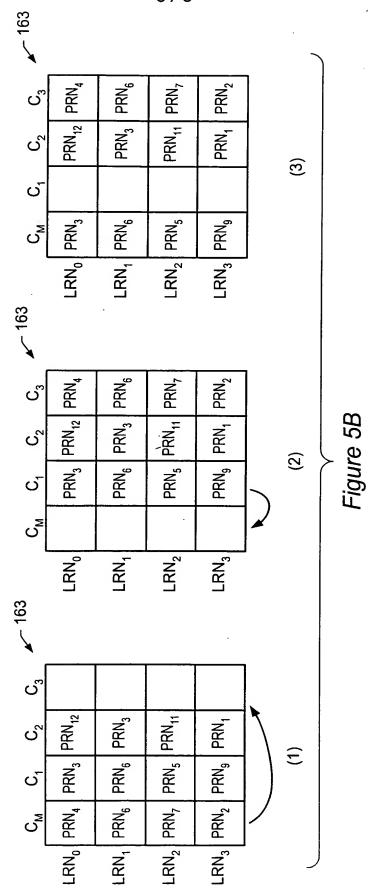
Figure 1

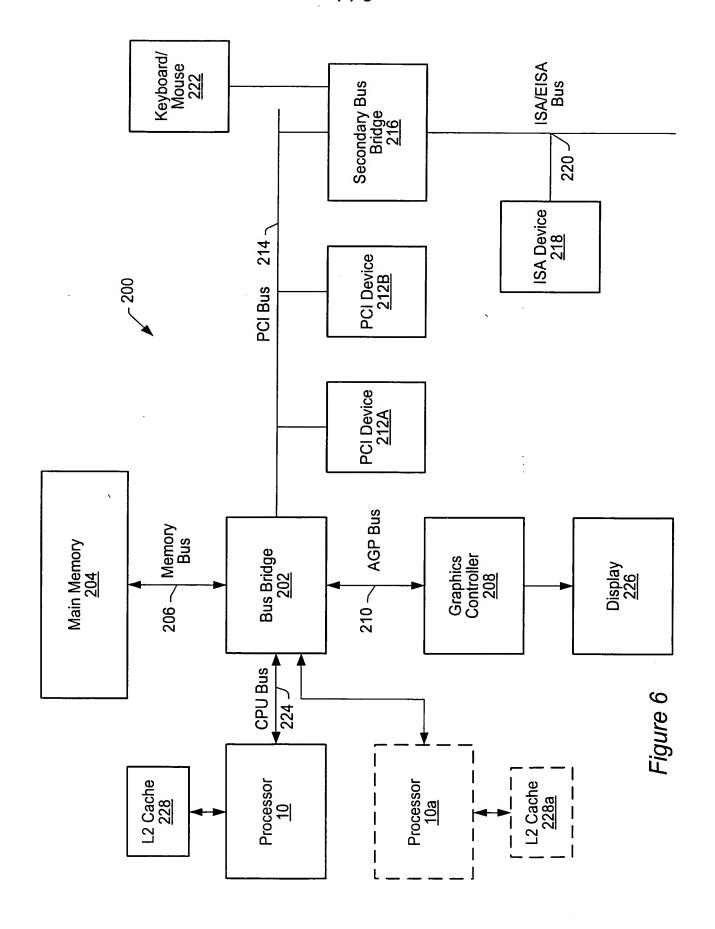




				m rt Pointer SRC _RN								
			C ₀	C ₁	C ₂	C ₃	C ₄	C ₅	C ₆	C ₇	C ₈	C ₉
Control Logic 163	PRN0	LRN0	1	1	1	0	0	0	0	0	0	0
	PRN1	LRN0	0	0	0	1	1	1	1	1	1	1
	PRN2	LRN1										
							•					
	•						•					
	-	-					•					
							•					
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	•	•					•					
	•	•										
	PRN _{N-1}	LRN										
							×	∽ 16	1			
PRN's							Figure 4					







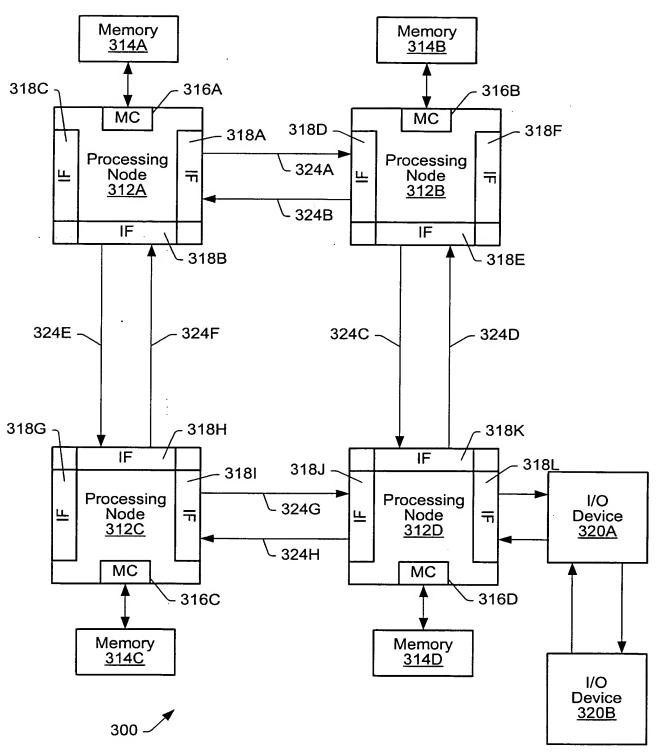


Figure 7